

Figure 1

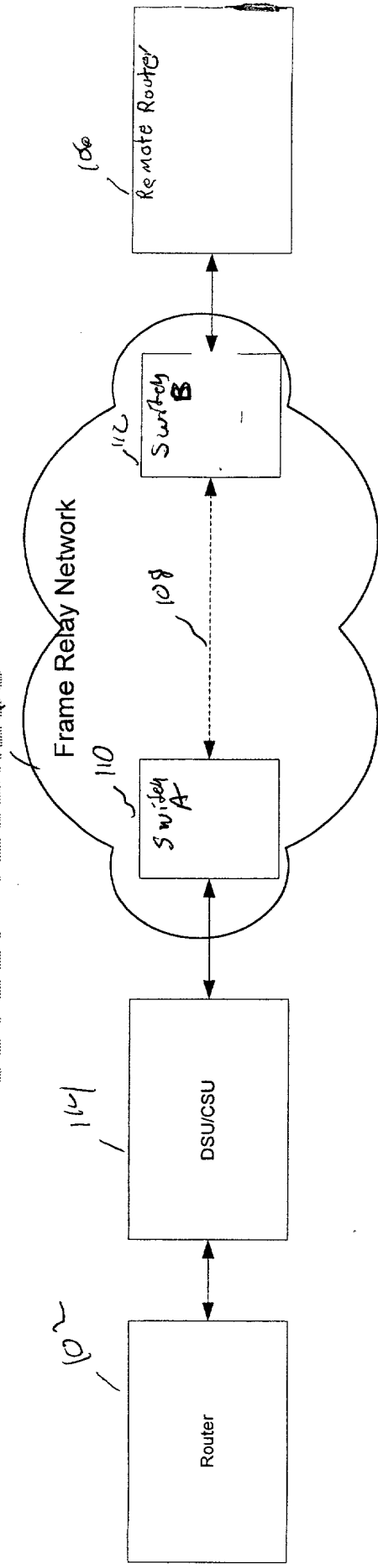


Fig. 2

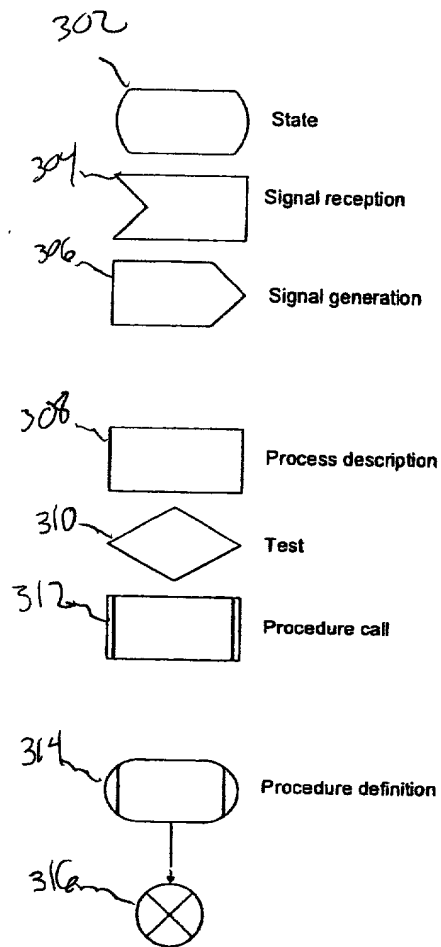


Fig. 3.

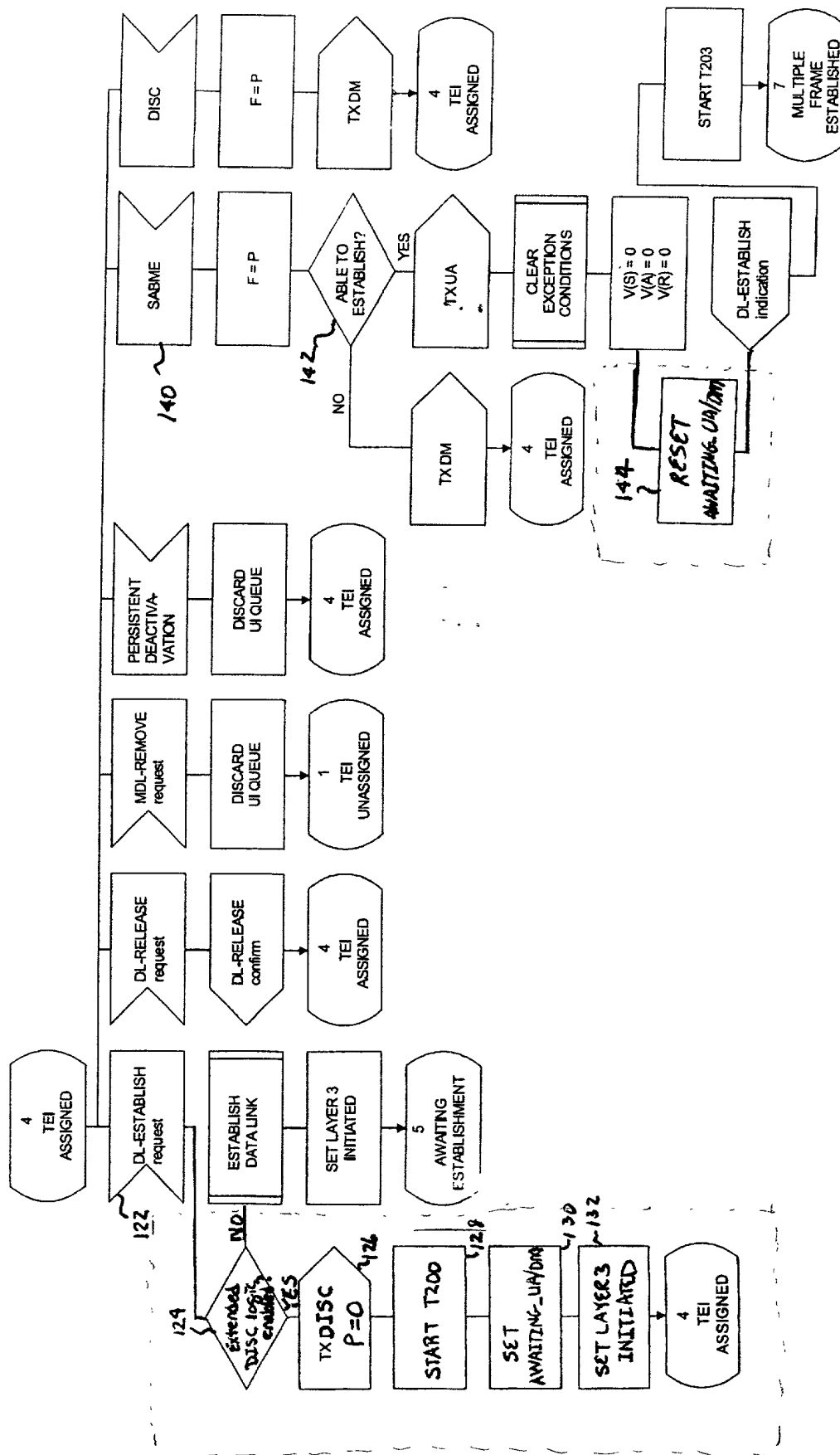
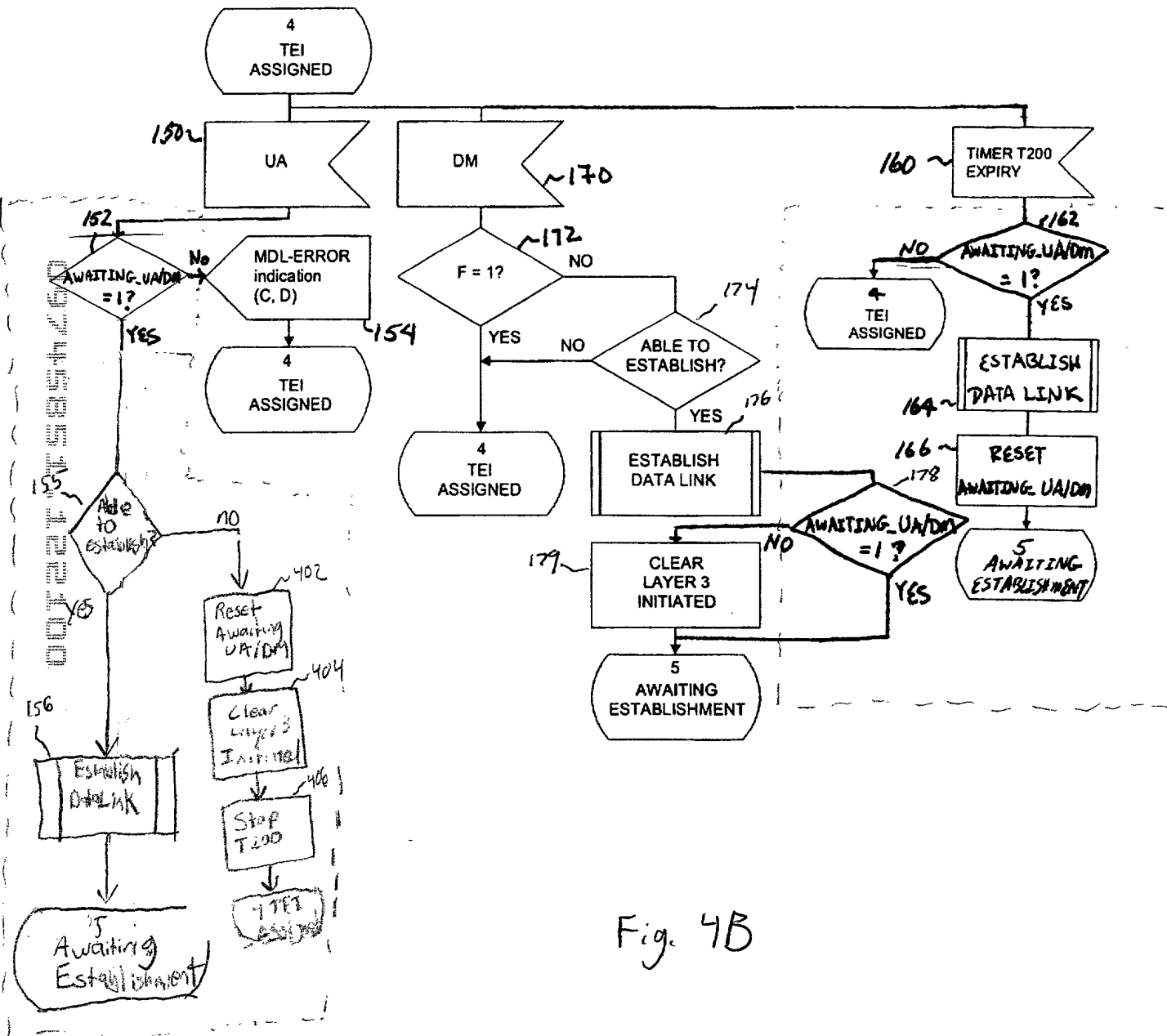


Fig. 4A



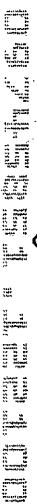


Fig. 5A

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graph TD
    Start([5 AWAITING ESTABLISHMENT]) --> Split(( ))
    Split --> Disc[/DISC/]
    Split --> UA[/UA/]
    
    Disc --> Fp[F = P]
    Fp --> P1{P = 1}
    P1 -- YES --> TXDM[/TX DM/]
    TXDM --> Wait5([5 AWAITING ESTABLISHMENT])
    P1 -- NO --> R1[RESET AND RELEASE]
    R1 --> V1["V(S) = V(A) + 1"]
    V1 --> Conf1[/DL-ESTABLISH confirm/]
    
    UA --> F1{F = 1}
    F1 -- NO --> MDL[/MDL-ERROR indication (D)/]
    MDL --> Wait5_2([5 AWAITING ESTABLISHMENT])
    F1 -- YES --> L3{LAYER 3 INITIATED?}
    L3 -- NO --> Veq{V(S) = V(A)?}
    Veq -- YES --> Conf1
    Veq -- NO --> DiscI[DISCARD I QUEUE]
    DiscI --> Ind1[/DL-ESTABLISH indication/]
    
    Conf1 --> StopT200[STOP T200  
START T203]
    Ind1 --> StopT200
    
    StopT200 --> V0["V(S) = 0  
V(A) = 0  
V(R) = 0"]
    V0 --> R2[RESET AWAITING UA/DN]
    R2 --> End7([7 MULTIPLE FRAME ESTABLISHED])
    
    R1 --> DiscIUI[DISCARD I AND UI QUEUES]
    DiscIUI --> DLRel[/DL-RELEASE indication/]
    DLRel --> StopT200_2[STOP T200  
STOP T203]
    StopT200_2 --> X((X))
  
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Fig. 5B

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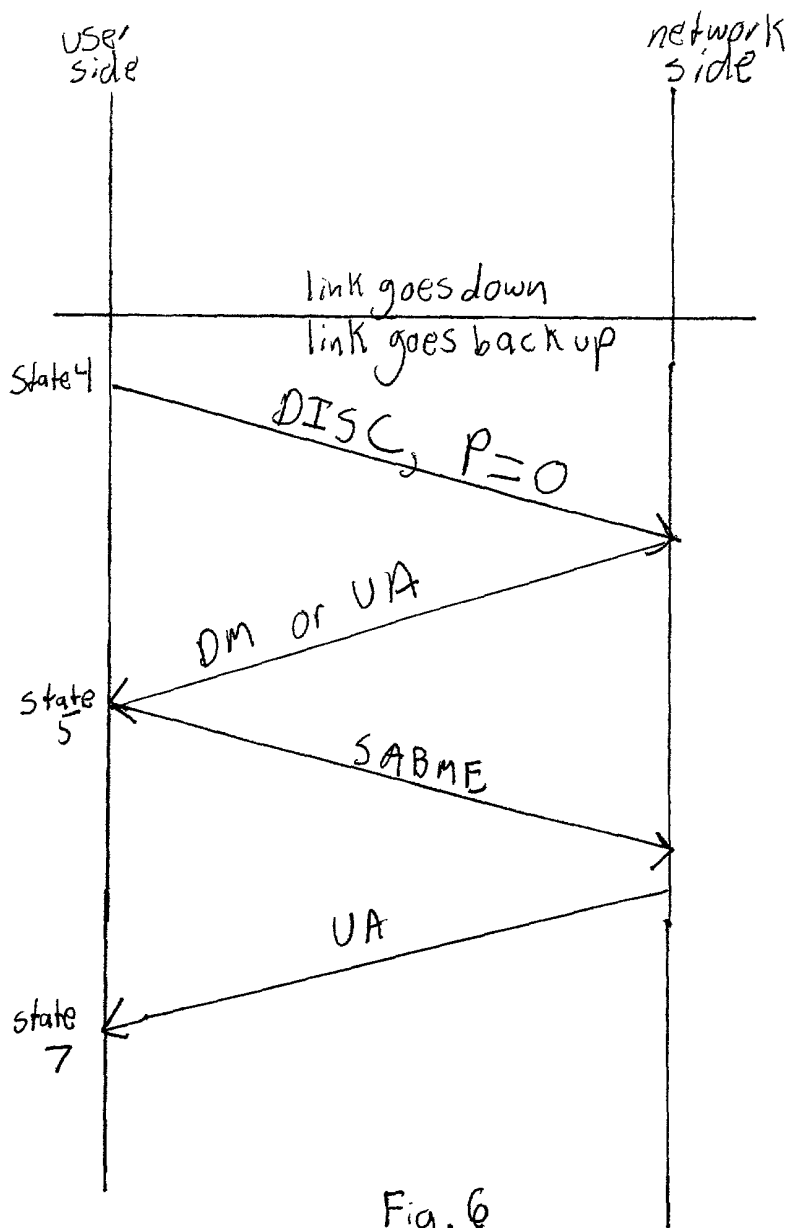


Fig. 6